

## AMENDMENTS

Please amend the application as indicated hereafter.

### In the Specification

Please substitute the following clean copy page/paragraph text for the pending page/paragraph text of the same number.

#### **Page 6, Paragraph 2:**

E1  
This application is related to U.S. Patent Application Serial No. 09/145,055, filed September 1, 1998, and entitled "DOPPLER CORRECTED SPREAD SPECTRUM MATCHED FILTER," now issued as U.S. Patent No. 6,044,105 and to U.S. Patent Application Serial No. 09/281,741, filed on March 30, 1999, and now issued as U.S. Patent No. 6,304,216 and entitled "SIGNAL DETECTOR EMPLOYING CORRELATION ANALYSIS OF NON-UNIFORM AND DISJOINT SAMPLE SEGMENTS," both of which are owned in common by the assignee hereof, and both of which are hereby fully incorporated by reference herein as though set forth in full.

#### **Pages 48-49, Paragraph 2:**

#### **VI. Implementation Example**

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A matched filter chip combines selected functionality of the sampling circuitry 308, the timing circuitry 307, and the matched filter 310 of FIGs. 8 and 10. In one implementation, the processor has additional GPS-specific circuits, such as tracking channels for continuously tracking a number of GPS satellite signals. Typically, the processor includes at least an embedded microprocessor with an external bus. In one configuration, the processor views the matched filter chip as a memory mapped peripheral. It issues commands to the matched filter chip, and retrieves results after it has completed processing for a given set of commands. An RF receiver chip embodies the functionality of the GPS radio receiver 300 of FIG. 4. Additional details regarding this

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implementation example are available in U.S. Patent No. 6,044,105, and in U.S. Patent No. 6,304,216, previously incorporated herein by reference.

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